SLR Systems

Z80ASM

ZILOG MNEMONIC RELOCATING MACRO ASSEMBLER

HIGH PERFORMANCE SOFTWARE

Z80ASM is becoming widely known as the fastest and most flexible Z80 assembler on the market. Whether you are looking for the fastest assembler on the block, or the one with the best features, or the best price, the SLR assemblers are for you.
See if any of these scenarios sound familiar:

You are leaving work a little late, frustrated because your project is behind schedule, and most of your time was spent waiting to reassemble each little change you made. Contrast that with leaving work a little late; not frustrated but so excited about your project being ahead of schedule that you hadn't noticed the time. Each little change you make during debugging is ready to try almost as soon as you edit the source. You are using Z80ASM.

You realize as you get further into your project that it would be much more manageable if you broke it up into smaller relocatable modules. As things progressed you would only need to list the modules that changed, only need to reassemble the modules that changed, only edit the smaller files. But, how could you live with just 6 character labels on your global symbols? You need Z80ASM for full 16 character globals.

If you would be satisfied leaving your large project in one file if your disk-based assembler didn't take all week to run, you need Z80ASM+.

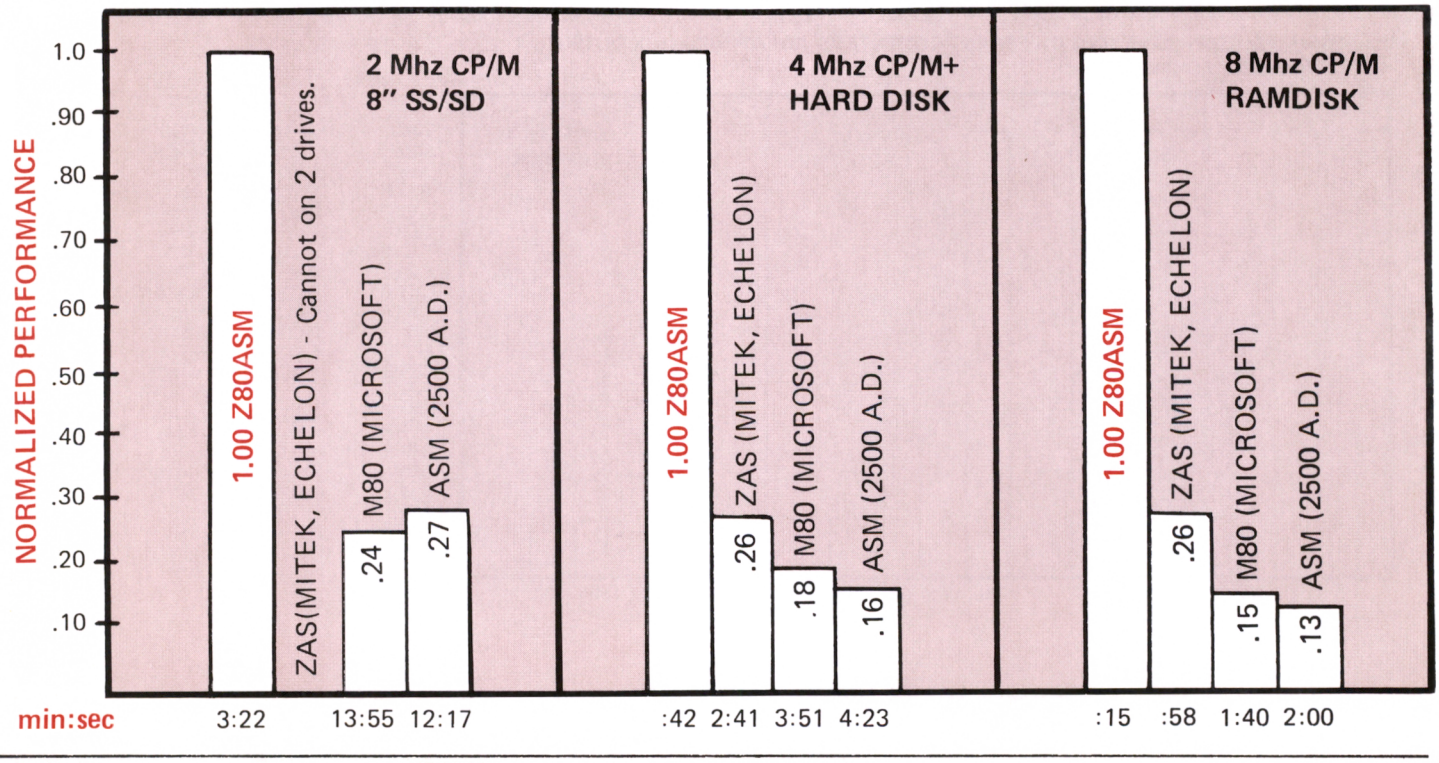
If you would like to get as many assembly turnarounds on your M80-.Z80 mode files by Monday afternoon that currently takes M80 until Friday, you will like Z80ASM.

Z80ASM & Z80ASM+ (the disk-based version) are the two fastest and most powerful Zilog mnemonic assemblers on the market. Besides being extremely fast, these mainframe caliber products tout an unparalleled collection of features such as the ability to produce COM, HEX, or REL file output directly, nested conditionals, nested include files, user selectable upper/lower case significance, labels significant to 16 characters, full Zilog mnemonic support, time and date in listings and much more.

Professional assembly language programmers will appreciate finding all of the following features in one product: M80/RMAC macro support, up to 15 separate program, data and common areas, sorted symbol table, sorted cross-reference, operator overview of the assembly process, phase-dephase support, math on externals, the ability to define symbols from the console, and many more.

WHY ALL THE RAVE?

WE ASSEMBLED ZCPR3 AND CREATED A LISTING & CROSS REFERENCE.



TECHNICAL SPECIFICATIONS

- ☐ Assembles to COM, HEX, or REL in one pass
- ☐ Intel macro facility
- ☐ M80 Compatible in .Z80 mode
- ☐ Accepts symbol definitions from console
- ☐ Nested conditionals and INCLUDE files
- ☐ Relocatable format supports math on externals
- ☐ Full ZILOG mnemonics
- ☐ Absolute, program, data, and common areas
- ☐ Fastest assembler on the market
- ☐ Sorted symbol and cross-reference tables
- ☐ Labels have 16 significant characters even on Externals
- ☐ Time and date in listing
- ☐ Assemble for execution at another address
- ☐ User configurable
- ☐ Colons not required on labels
- ☐ Reserved words may be redefined as macros and/or labels
- ☐ Z80ASM+ - All internal tables overflow to disk meaning tables are limited to disk size, not TPA size

COMMAND LINE OPTIONS

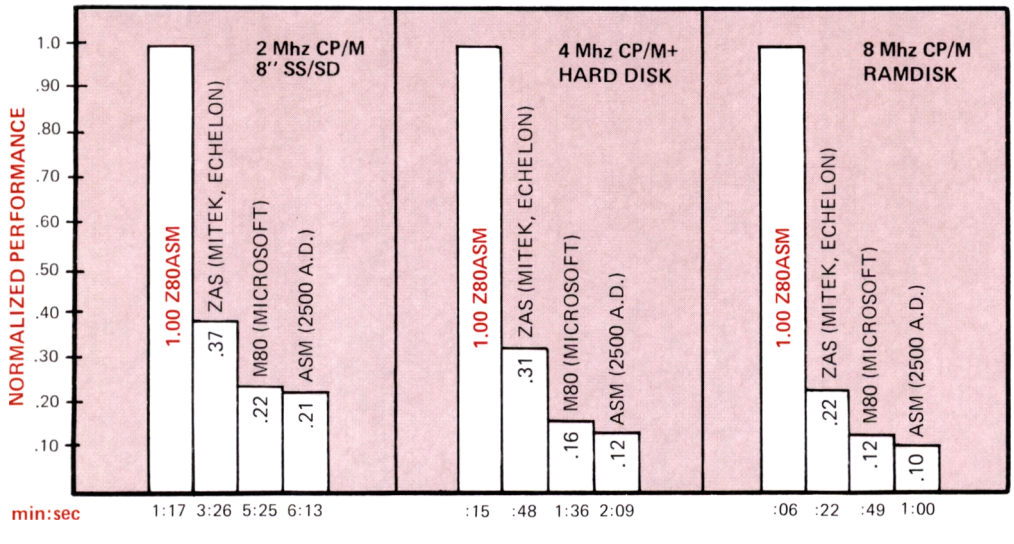
- ☐ Select COM, HEX, SLR Rel, or Microsoft Rel output
- ☐ Select listing output type and output devices
- ☐ Select/Deselect upper/lower case distinction
- ☐ Select 1 or 2 pass mode
- ☐ Select indirect command file
- ☐ Disallow console I/O and auto-detach
- ☐ Select symbol table and/or xref
- ☐ Select auto-declaration of externals
- ☐ Select 6 or 7 characters in Microsoft Rel format

CONFIGURE OPTIONS

- ☐ Set default command line switch settings
- ☐ Set default printer page length and width
- ☐ Control listing of more than 4 bytes of code per source line
- ☐ Set macro listing default
- ☐ Set byte order for printing 16-bit values
- ☐ Flag for suppressing PAGE and TITLE lines in listing
- ☐ Select time/date in listing, with options for clocks not supported directly by the operating system.
- ☐ Use/do not use multi-sector I/O
- ☐ Define listing format
- ☐ Set error page limit and abort limit
- ☐ Number of bytes per HEX record
- ☐ Set all file extensions
- ☐ Set printer initialization and trailer strings

	Z80ASM	ZAS (Mitek/ Echelon)	M80 (Microsoft)	ASM (2500 AD)
Zilog mnemonics	Yes	Yes	Yes	Yes
M80 .Z80 mode compatible	Yes	No	Yes	No
Direct COM output	Yes	No	No	No
Microsoft compatible REL	Yes	Yes	Yes	No
Significant chars on labels	16	16	16	10
Significant chars on EXTRNS	16	6	6	10
Math on externals	Yes	No	Some	No
Time and date in listing	Yes	No	No	No
User configurable options	40	7	0	0
Abort capability	Yes	Yes	No	Yes
Fastest on the market	Yes	No	No	No

WE ASSEMBLED ZCPR3.Z80 ON 3 DIFFERENT COMPUTERS CREATING A HEX OUTPUT.
HERE'S WHAT WE FOUND:



PSEUDO-OP LIST

ABS	ELSE	IFNB	TITLE
ASEG	END	IFNDEF	*EJECT
ASET	ENDC	IFT	.ACCEPT
BYTE	ENDIF	INCLUDE	.COMMENT
CLIST	ENDM	IRP	.CREF
COM	ENT	IRPC	.DEPHASE
COMMON	ENTRY	LIST	.LALL
COND	EQU	LOCAL	.LFCOND
CSEG	EXITM	MACLIB	.LIST
DATA	EXT	MACRO	.PHASE
DB	EXTERNAL	MEND	.PRINTX
DC	EXTRN	MTLIST	.RADIX
DEFB	GLOBAL	NAME	.REQUEST
DEFC	IF	NCLIST	.SALL
DEFL	IF0	NLIST	.SFCOND
DEFM	IF1	NMTLIST	.TFCOND
DEFS	IF2	ORG	.XALL
DEFW	IFB	PAGE	.XCREF
DEFZ	IFDEF	PROG	.XLIST
DS	IFDIF	PUBLIC	.Z80
DSEG	IFE	REL	$EJECT
DW	IFF	REPT	$INCLUDE
EJECT	IFIDN	SUBTTL	$TITLE

SLR Systems

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